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A practical and fascinating book on a topic at the forefront of communications technology. Field-Programmable Gate Arrays (FPGAs) are on the verge of revolutionizing digital signal processing. Novel FPGA families are replacing ASICs and PDSPs for front-end digital signal processing algorithms at an accelerating rate. The efficient implementation of these algorithms is the main goal of this book. It starts with an overview of today's FPGA technology, devices, and tools for designing state-of-the-art DSP systems. Each of the book's chapter contains exercises. The VERILOG source code and a glossary are given in the appendices.

This text serves as an introduction to, and a survey of, the common commercial architectures. It was created with a strong electrical and computer engineering perspective, including current topics such as pipelined processor design, memory hierarchy and in

Market_Desc: · Computer Engineers· Systems Administrators Special Features: · Connects the programmer's view of a computer system with the architecture of the underlying machine.· Describes network architectures, focusing on both local area networks and wide area networks.· Explores advanced architectural features that have either emerged or taken · Places topics into perspective by introducing case studies in every chapter About The Book: Taking an integrated approach, this book addresses the great diversity of areas that a computer professional must know. It exposes the inner workings of the modern digital computer at a level that demystifies what goes on inside the machine. Throughout the pages, the authors focus on the instruction set architecture (ISA), the coverage of network-related topics, and the programming methodology. Each topic is discussed in the context of the entire machine and how the implementation affects behavior.

Com es pot dissenyar un petit microprocessador a partir de mòduls molt bàsics? Com és el funcionament intern d'un microprocessador? Què és una màquina algorísmica? Quines són les parts principals d'una unitat de control microprogramada? Per adquirir els coneixements necessaris per respondre aquestes i altres preguntes relacionades, un estudiant pot assistir a classes teòriques (bla, bla, bla), fer llargs exercicis sobre el paper (i com verifico el resultat?), memoritzar conceptes (vaja, ja no me'n recordo)... però, per damunt de tot, són la pràctica i l'experiència directa i continuada els mecanismes que s'erigeixen com els més efectius en el procés d'aprenentatge. Just això és el que ha portat als autors a definir una metodologia eminentment pràctica per a l'adquisició d'amplis coneixements sobre les màquines algorísmiques. Desenvolupament del microprocessador LittleProc... és el resultat d'un bon nombre d'anys d'experiència docent en els quals els autors han pogut comprovar i optimitzar l'efectivitat d'aquesta metodologia. Durant aquest període han constatat com el repte/aventura de desenvolupar un petit processador, LittleProc, al llarg d'un semestre, des del seu disseny i simulació fins a la seva implementació en una placa de prototipatge, captava plenament l'interès dels estudiants i els permetia aprofundir en el coneixement de les màquines algorísmiques i, en general, dels sistemes digitals. Aquest llibre, doncs, està especialment dirigit als professors que imparteixen assignatures vinculades al món dels sistemes digitals. Per a ells pot resultar summament interessant conèixer la metodologia descrita en aquest llibre i incorporar-la o assumir-la plenament en la part pràctica de les seves assignatures. A més, de fer-ho així, els professors es poden beneficiar d'una de les principals utilitats del llibre: ésser usat pels estudiants com a guió de treball durant les successives sessions de pràctiques, simplificant-ne la seva realització. Una exclamació, extreta d'una conversa amb els autors, resumeix perfectament l'esperit amb què està fet aquest llibre: «Que comencin els estudiants a desenvolupar LittleProc, que comencin per a ells el repte i l'aventura ja!».

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