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Simplify the
following Boolean
function F , together
with the don't-care
conditions d , and

Q. 4.27: A

combinational
circuit is specified

by the following
three Boolean

functions Q. 3.6:

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~~Simplify the following Boolean expressions, using four variable maps:~~

~~(a) $A'B'C'D' + AC'D'$~~

~~+ Q. 2.16: The logical sum of all minterms of a Boolean function of n variables is 1. (a) Prove the Implementation of boolean function using multiplexer in~~

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Chandrayaan-2:
95% mission has
been successful,
says ISRO Q. 2.19:
Express following
function as sum of
minterms and
product of

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maxterms: $F = B'D + A'D + BD$ Q.

3.12: Simplify the following Boolean functions to product-of-sums form: (a) $F(w,x,y,z)$

$= \text{sum}(0,1,2, \dots)$ MIPS Architecture Basics

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4.34: Implement the following Boolean function with a multiplexer (HDL—see Problem 4.48) ~~Q. 5.4: A PN flip-flop has four operations: clear to 0, no change, complement, and set to 1, when~~

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4.10: Design a four-bit combinational circuit 2's complementer.

(The output generates the 2's

Q. 3.9: Find all the prime implicants for the following

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Boolean functions,
and determine
which are

7- Shift Add

Multiplier Version 2

| Shift Add

Multiplication

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5.8)

Q. 6.24: Design a

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counter with T
flip-flops that goes
through the
following binary
repeated sequence

~~Q. 2.14: Implement
the Boolean~~

~~function $F = xy +$~~

~~$x'y' + y'z$ (a) With~~

~~AND, OR, and~~

~~inverter gates~~

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